

## 1500V N-Channel MOSFET

### Product Description

$BV_{DSS}$	1500	V
$I_D$	3.0	A
$R_{DS(ON),Typ.}$	5.4	$\Omega$

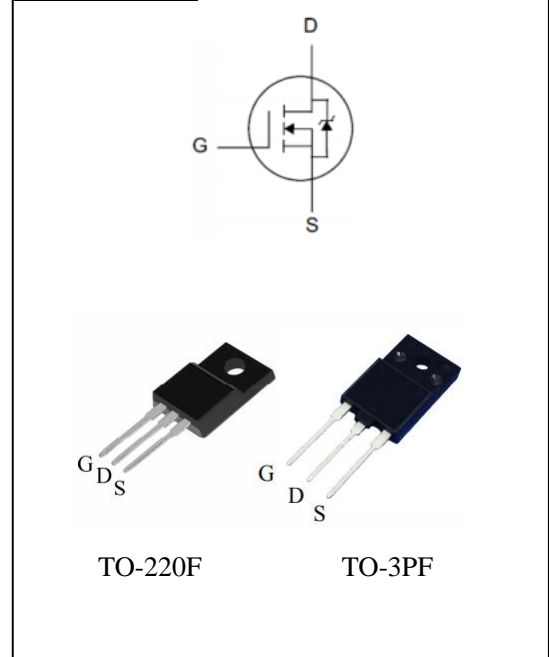
### General Features

- RoHS Compliant
- $R_{DS(ON),typ.}=5.4\Omega$  @  $V_{GS}=10V$
- Fast Recovery Body Diode
- Low Gate Charge Minimize Switching Loss

### Applications

- Charger
- Adaptor
- SMPS Standby Power

### 封装 Package



Part Number	Package
HYAF03N150	TO-220F
HYHF03N150	TO-3PF

### Absolute Maximum Ratings $T_j=25^\circ C$

Symbol	Parameter	Value		Unit
		TO-220F	TO-3PF	
$V_{DSS}$	Drain-to-Source Voltage	1500		V
$V_{GSS}$	Gate-to-Source Voltage	$\pm 30$		
$I_D$	Continuous Drain Current	3		A
$I_{DM}$	Pulsed Drain Current at $V_{GS}=10V$	12		
$E_{AS}$	Single Pulse Avalanche Energy	500		mJ
$P_D$	Power Dissipation	35	90	W
	Derating Factor above $25^\circ C$	0.28	0.72	
$T_L$	Soldering Temperature Distance of 1.6mm from case for 10 seconds	300		$^\circ C$
$T_J \& T_{STG}$	Operating and Storage Temperature Range	-55 to 150		

Caution: Stresses greater than those listed in the “Absolute Maximum Ratings” may cause permanent damage to the device.

### Thermal Characteristics

Symbol	Parameter	Value		Unit
		TO-220F	TO-3PF	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	3.57	1.38	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	100	50	$^{\circ}\text{C}/\text{W}$

### Electrical Characteristics $T_J=25^{\circ}\text{C}$

#### OFF Characteristics

Symbol	Parameter	Min	Typ	Max	Unit	Test Condition
$BV_{DSS}$	Drain-to-Source Breakdown Voltage	1500	-	-	V	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$
$I_{DSS}$	Drain-to-Source Leakage Current	-	-	1	uA	$V_{DS}=1500\text{V}, V_{GS}=0\text{V}$
		-	-	100		$V_{DS}=1200\text{V}, V_{GS}=0\text{V}, T_J=125^{\circ}\text{C}$
$I_{GSS}$	Gate-to-Source Leakage Current	-	-	+100	nA	$V_{GS}=+30\text{V}, V_{DS}=0\text{V}$
		-	-	-100		$V_{GS}=-30\text{V}, V_{DS}=0\text{V}$

#### ON Characteristics

Symbol	Parameter	Min	Typ	Max	Unit	Test Condition
$R_{DS(ON)}$	Static Drain-to-Source On-Resistance	-	5.4	8.2	$\Omega$	$V_{GS}=10\text{V}, I_D=1.5\text{A}$
$V_{GS(TH)}$	Gate Threshold Voltage	2.5	-	4.5	V	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$
gfs	Forward Transconductance	-	3	-	S	$V_{DS}=15\text{V}, I_D=3\text{A}$

**Dynamic Characteristics**

Symbol	Parameter	Min	Typ	Max	Unit	Test Condition
$C_{iss}$	Input Capacitance	-	1600	-	pF	$V_{GS}=0V$ , $V_{DS}=25V$ , $f=1.0MHz$
$C_{rss}$	Reverse Transfer Capacitance	-	33	-		
$C_{oss}$	Output Capacitance	-	100	-		
$Q_g$	Total Gate Charge	-	36	-	nC	$V_{DD}=750V$ , $I_D=3A$ , $V_{GS}=0$ to $10V$
$Q_{gs}$	Gate-to-Source Charge	-	9.5	-		
$Q_{gd}$	Gate-to-Drain (Miller) Charge	-	12	-		

**Resistive Switching Characteristics**

Symbol	Parameter	Min	Typ	Max	Unit	Test Condition
$t_{d(ON)}$	Turn-on Delay Time	-	25	-	ns	$V_{DD}=600V$ , $I_D=3A$ , $V_{GS}=10V$ $R_g=4.7\Omega$
$t_{rise}$	Rise Time	-	48	-		
$t_{d(OFF)}$	Turn-Off Delay Time	-	57	-		
$t_{fall}$	Fall Time	-	52	-		

**Source-Drain Body Diode Characteristics**

Symbol	Parameter	Min	Typ	Max	Unit	Test Condition
$I_{SD}$	Continuous Source Current <sup>[1]</sup>	-	-	3	A	Integral pn-diode in MOSFET
$I_{SM}$	Pulsed Source Current <sup>[1]</sup>	-	-	12		
$V_{SD}$	Diode Forward Voltage	-	-	1.5	V	$I_S=3A$ , $V_{GS}=0V$
$t_{rr}$	Reverse Recovery Time	-	255	-	ns	$V_{GS}=0V$ $I_F=3A$ , $di_F/dt=100A/\mu s$
$Q_{rr}$	Reverse Recovery Charge	-	1100	-	nC	

[1] Pulse width $\leq 380\mu s$ ; duty cycle $\leq 2\%$

## Typical Characteristics

Figure 1. Maximum Transient Thermal Impedance

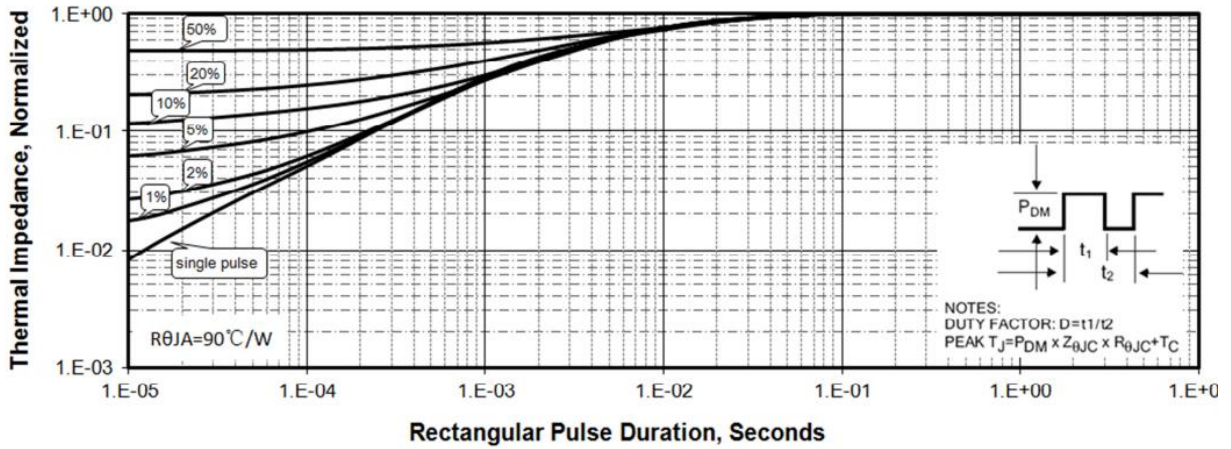


Figure 2 . Max. Power Dissipation vs Case Temperature

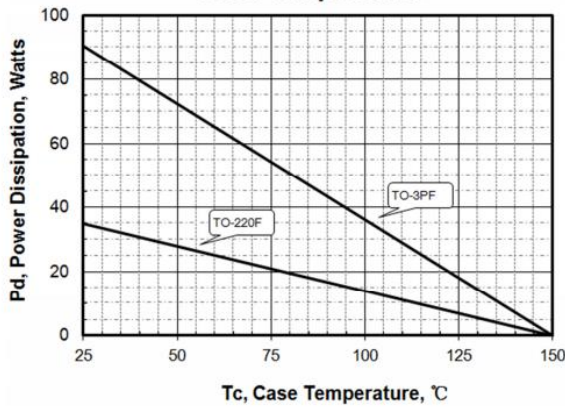


Figure 3 .Maximum Continuous Drain Current vs Tc

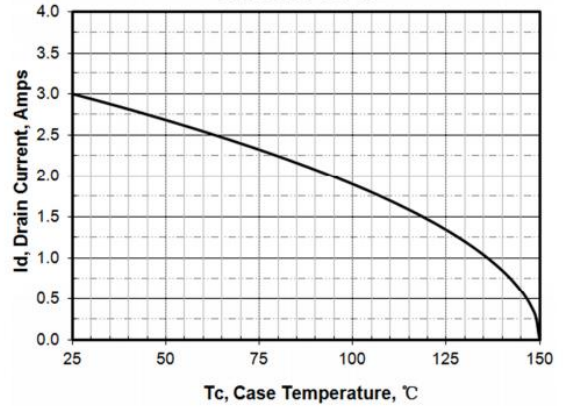


Figure 4. Output Characteristics

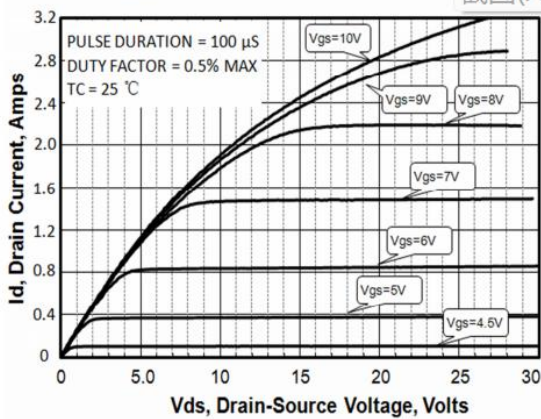
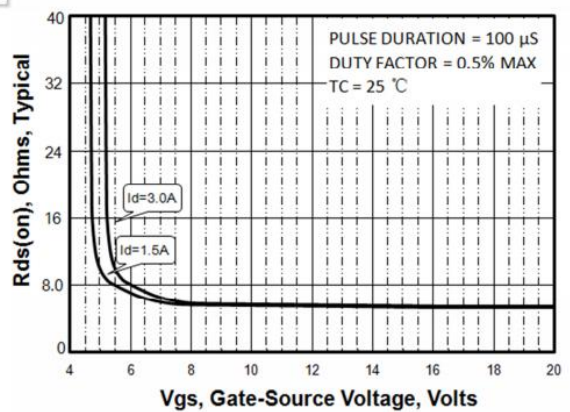
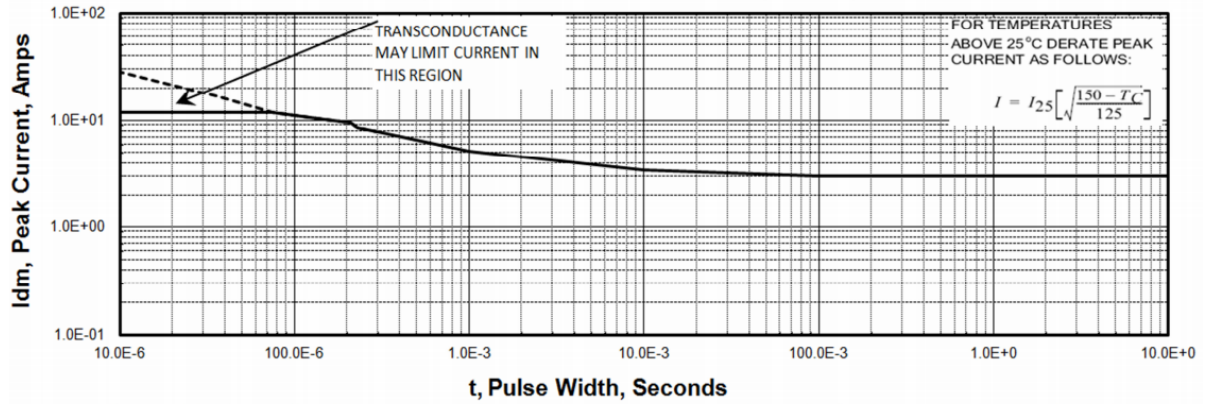
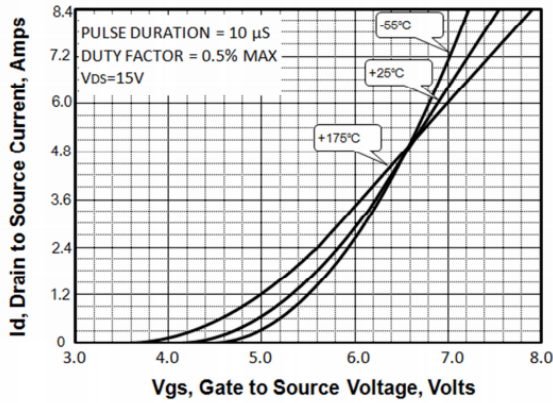
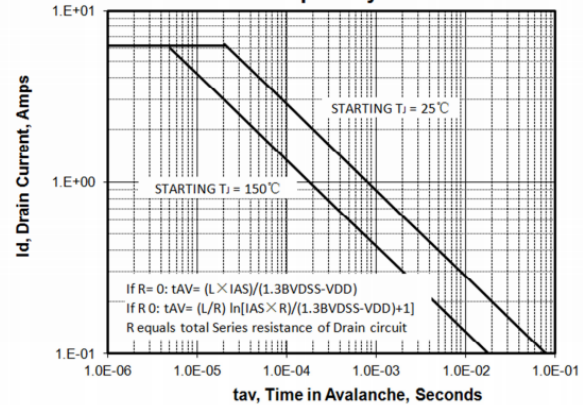
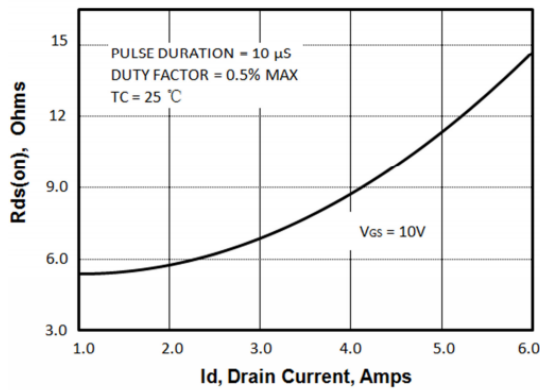
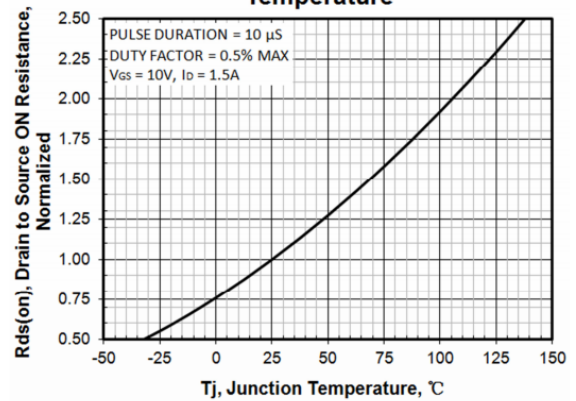
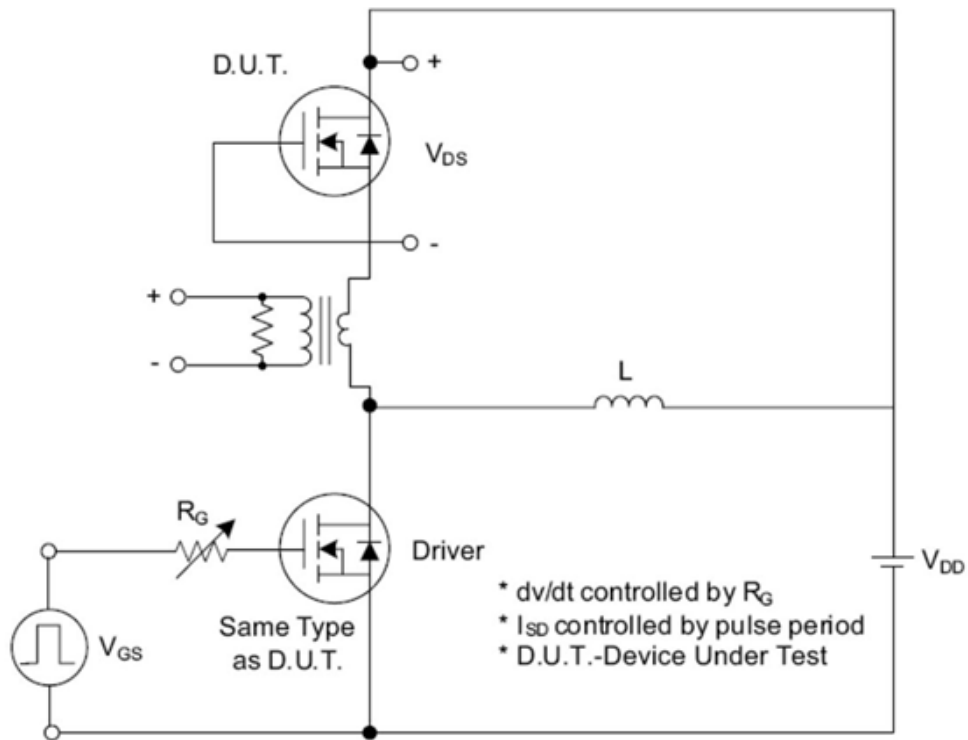
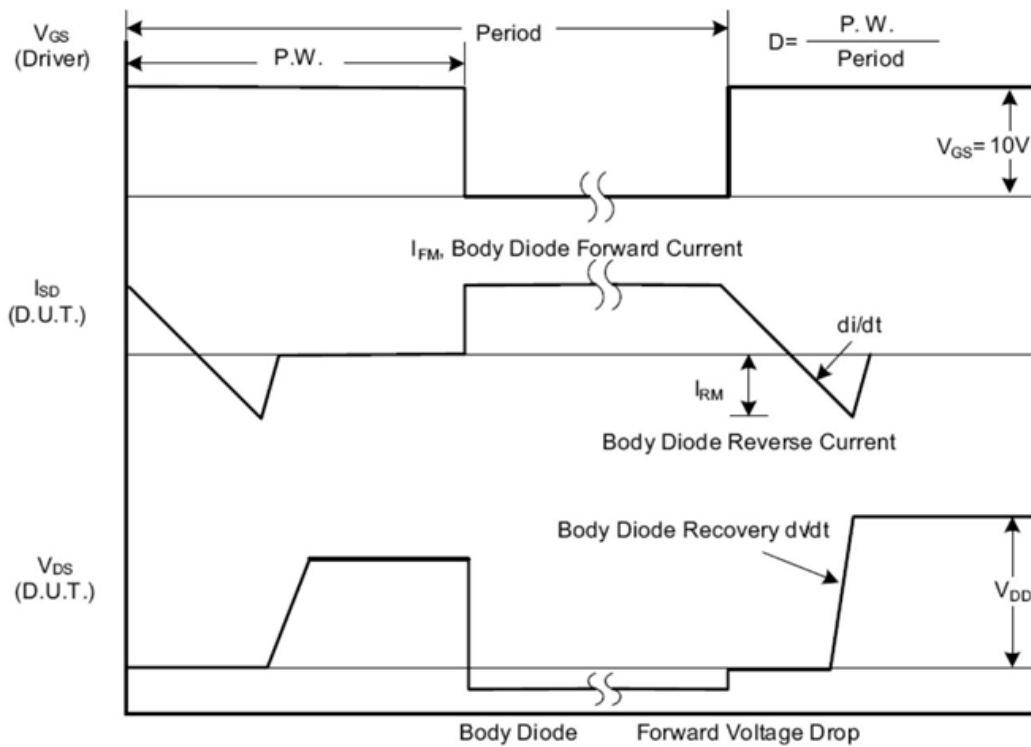


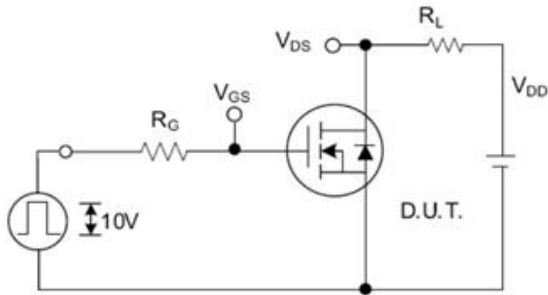
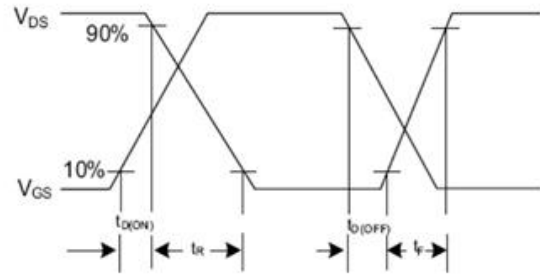
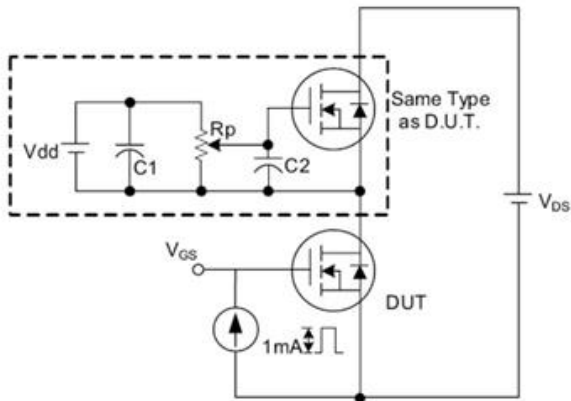
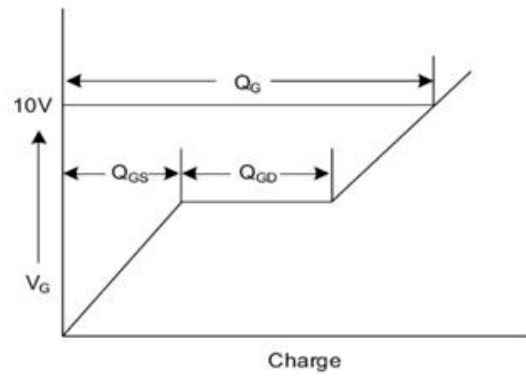
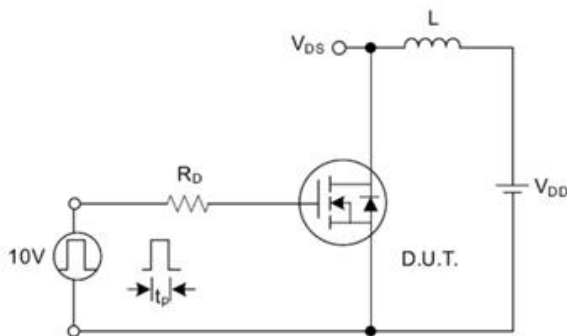
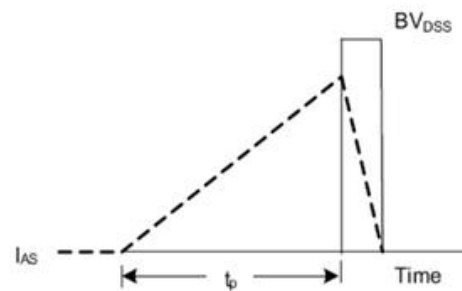
Figure 5. Rds(on) vs Gate Voltage



## Typical Characteristics

**Figure 6. Peak Current Capability**

**Figure 7. Transfer Characteristics**

**Figure 8. Unclamped Inductive Switching Capability**

**Figure 9. Drain to Source ON Resistance vs Drain Current**

**Figure 10. Rds(on) vs Junction Temperature**


**Test Circuits and Waveforms**

 Fig. 1.1 Peak Diode Recovery  $dv/dt$  Test Circuit

 Fig. 1.2 Peak Diode Recovery  $dv/dt$  Waveforms

**Test Circuits and Waveforms (Cont.)**

**Fig. 2.1 Switching Test Circuit**

**Fig. 2.2 Switching Waveforms**

**Fig. 3.1 Gate Charge Test Circuit**

**Fig. 3.2 Gate Charge Waveform**

**Fig. 4.1 Unclamped Inductive Switching Test Circuit**

**Fig. 4.2 Unclamped Inductive Switching Waveforms**